

1 Implementation of AES with Time Complexity Measurement for 2 Various Input

3 Shraddha More¹

4 ¹ Mumbai university

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6

7 **Abstract**

8 Network Security has a major role in the development of data communication system, where
9 more randomization in the secret keys increases the security as well as the complexity of the
10 cryptography algorithms. In the recent years network security has become an important issue.
11 Cryptography has come up as a solution which plays a vital role in the information security
12 system against various attacks. This security mechanism uses the AES algorithm to scramble
13 data into unreadable text which can only be decrypted with the associated key. The AES
14 algorithm is limited only for text as an input. It also has, the more time complexity. So it
15 suffers from vulnerabilities associated with another type of input and time constraints. So its
16 challenge to implement the AES algorithm for various types of input and require less
17 decryption time. The propose work demonstrate implementation of a 128-bit Advanced
18 Encryption Standard (AES), which consists of both symmetric key encryption and decryption
19 algorithms for input as a text, image and audio. It also gives less time complexity as
20 compared to existing one. At the last stage comparing the time complexity for encryption and
21 decryption process for all three types of input. This paper also demonstrates a side channel
22 attack on the standard software implementation of the AES cryptographic algorithm.de

23

24 **Index terms**— side channel attack, aes, des, rsa, encryption, decryption, cryptography, network security..

25 **1 ImplementationofAESwithTimeComplexityMeasurementforVariousInp**

26 Strictly as per the compliance and regulations of: Introduction ryptography plays an important role in the security
27 of data transmission. Data Security is a challenging concern of data communications that focuses on many
28 areas including secure communication channel and strong data encryption technique. The secure transmission
29 of confidential data enclosed gets a great deal of attention because of the rapid development in information
30 technology. The predictable methods of encryption can only maintain the data security. The development of
31 computing technology imposes stronger requirements on the cryptography schemes. The rapidly growing number
32 of wireless communication users has led to the increasing demand for security measures and devices to protect
33 user data transmitted over wireless channels [1].

34 Two types of cryptographic systems have been developed for that purpose symmetric (secret key) and
35 asymmetric (public key) cryptosystems. Symmetric cryptography, such as in the Data Encryption Standard
36 (DES), 3DES, and Advanced Encryption Standard (AES) uses an identical key of the sender to encrypt the
37 message text and receiver to decrypt the encrypted text. Asymmetric cryptography, such as the Rivest-Shamir-
38 Adleman (RSA) uses different public keys for encryption and decryption, eliminating the key exchange problem.
39 [2] Symmetric cryptography is more suitable for the encryption of a large amount of data. The Data Encryption
40 Standard (DES) has been used by the U.S. government standard since 1977. However, now, it can be cracked
41 quickly and inexpensively. The AES algorithm defined by the National Institute of Standards and Technology
42 (NIST) of the United States has widely accepted to replace DES as the new symmetric encryption algorithm [3].

7 ADDROUNDKEY

43 This above cryptographic algorithms are not more secure. To overcome the vulnerabilities in network security in
44 2000, the Advanced Encryption Standard (AES) replaced the DES to meet the ever-increasing requirements for
45 security. In cryptography, the AES, also called as Rijndael, is a block cipher adopted as an encryption standard by
46 the US government, which specifies an encryption algorithm capable of protecting sensitive information [4]. The
47 Rijndael algorithm is a symmetric block cipher that can encrypt and decrypt information. Encryption converts
48 data into an unintelligible form called cipher-text. Decryption of the cipher-text converts the data back into its
49 unique form which is called plaintext. The AES algorithm supports 128, 192 and 256 bit key length to encrypt
50 and decrypt data in blocks of 128 bits , hence the name AES-128, AES-192 and AES-256 respectively [5]. The
51 hardware implementation of the AES algorithm can provide high performance, low cost for specific applications
52 and trustworthiness compared to its software counterparts [6].

53 The organization of the paper is as follows, Section II describes the design overview of AES algorithm for both
54 encryption and decryption. Section III presents implementation Details, Section IV is discussed on Experimental
55 Results. Section V projects on future scope and conclusion.

56 2 II.

57 3 Design Overview of aes

58 AES is a symmetric block cipher with block length of 128 bits. It allows three different key lengths 128,192
59 and 256 bits. In encryption process processing of 128 bit keys required for 10 rounds, 192 bit keys required for
60 12 rounds and 256 bit keys required for 14 rounds which is shown in table1. AES is a round based algorithm.
61 For encryption and decryption each round has four functions excepting last round. Last round required three
62 functions. The encryption algorithm has four round functions SubByte(), ShiftRows(), MixColumn()and
63 AddRoundKey(). The decryption, also has the same number of rounds with reverse transformation, order of
64 round function is different i.e. InvShiftRow(), InvSubByte(), AddRoundKey() and InvMixColumn() [2]- [3].

65 4 a) AES Encryption Algorithm

66 The Encryption process consists of a number of different transformations applied consecutively over the data
67 block bits in a fixed number of iterations which is called as rounds. The number of rounds depends on the length
68 of the key used for the encryption process. 10 iterations are required for key length of 128 bits.

69 i. High-level description of the algorithm KeyExpansions -round keys are derived from the cipher key using
70 Rijndael's key schedule. AES requires a separate 128-bit round key block for each round plus one more.
71 ii. InitialRound 1. AddRoundKey()-Each byte of the state is combined with a block of the round key using
72 bitwise xor. Rounds 2. SubBytes()-A non-linear substitution step where each byte is replaced with another
73 according to a lookup table. 3. ShiftRows()-A transposition step where the last three rows of the state are
74 shifted cyclically a certain number of steps.

75 5 MixColumns()-A mixing operation which operates

76 on the columns of the state, combining the four bytes in each column. iii. Final Round (No MixColumns)
77 SubBytes() ShiftRows() AddRoundKey().

78 Steps : These steps are used to encrypt128-bit block. 1. The set of round keys from the cipher key. 2. Initialize
79 state array and add the initial round key to the starting state array. 3. Perform round = 1 to 9 : Execute Usual
80 Round.

81 4. Execute Final Round.

82 6 Corresponding cipher text chunk output of Final Round

83 Step iv. Encryption process Each round consists of the following four steps: SubBytes Transformation: In this
84 transformation, each of the byte in the state matrix is replaced with another byte as per the S-box (Substitution
85 Box) [7]. The S-box is generated by firstly calculating the respective reciprocal of that byte in GF (2⁸) and
86 then affine transform is applied. ShiftRows Transformation: In this transformation, the bytes in the first row of
87 the State do not change. The second, third, fourth and fifth rows shift cyclically to the left by one byte, two
88 bytes, three bytes and four bytes respectively [7]. MixColumns Transformation: It is the operation that mixes
89 the bytes in each column by the multiplication of the state with a fixed polynomial matrix [7]. It completely
90 changes the scenario of the cipher even if all bytes look very similar. The Inverse Polynomial Matrix does exist
91 in order to reverse the mix column transformation.

92 7 AddRoundKey

93 Transformation:

94 In AddRoundKey transformation, a roundkey is added to the State by bitwise Exclusive-OR (XOR) operation.
95 AddRoundKey proceeds onecolumn at a time. AddRoundKey adds a roundkey word with each state column
96 matrix.The operation performed in AddRoundKey is matrix addition.

97 8 b) AES Decryption Algorithm

98 Decryption is the process of extracting the plaintext from cipher text. For decryption the same process occurs
99 simply in reverse order by taking the 128-bit block of cipher text and converting it to plaintext by the application
100 of the inverse of the four operations. Decryption involves reversing all the steps taken in encryption using following
101 inverse functions. InvSubBytes Transformation: InvSubBytes is the inverse transformation of SubBytes, in which
102 the inverse S-box is applied to individual bytes in the State. The inverse Sbox is constructed by first applying
103 the inverse of the affine transformation, then computing the multiplicative inverse in $GF(2^8)$.

104 InvShiftRows Transformation: InvShiftRows is the inverse transformation of ShiftRows. In this transformation,
105 the bytes in the first row of the State do not change. The second, third, and fourth and fifth rows are shifted
106 cyclically by one byte, two bytes, three bytes and four bytes to the right respectively [2]. InvMixColumns
107 Transformation: InvMixColumns is the inverse transformation of MixColumns. This is a complex procedure as
108 it involves severely the byte multiplication under $GF(2^8)$ [2].

109 9 Key Expansion (Keyexpansion Operation)

110 Keyexpansion refers to the process in which the 128 bits of the original key are expanded into eleven 128-bit
111 round keys.

112 To compute round key $(n+1)$ from round key (n) these steps are performed: 1. Compute the new first column
113 of the next round key.

114 First all the bytes of the old fourth column have to be substituted using the Subbytes operation. These four
115 bytes are shifted vertically by one byte position and then XORed to the old first column. The result of these
116 operations is the new first column. The key expansion algorithm generates 128 bit key for each round and one
117 more key for initial AddRoundKey function. The same expanded key is used for encryption and decryption
118 except for decryption it reads in reverse order.

119 10 III.

120 11 Implementation Details

121 The system proposing aims to achieve network security by implementing appropriate countermeasures based on
122 concept of constant time encryption against side channel timing attack to protect implementations of secret key
123 cryptography. The contribution work includes implementing more suitable countermeasures against side channel
124 attack.

125 The propose system, is intended to provide secure transmission of data over the network by implementing
126 the appropriate countermeasures against side channel attack on AES implementation which is shown in Fig.
127 2. Here the work implementing AES 128bit algorithm using 10 rounds by taking input as text, image and
128 audio. In AES encryption process, system performs round functions like SubByte(), ShiftRows(), MixColumn()
129 and AddRoundKey(). On the other side, the decryption process performs round functions like InvShiftRow(),
130 InvSubByte(), AddRoundKey() and InvMixColumn(). After that the work implementing side channel
131 attack on the AES implementation in such a way that the receiver cannot decrypt the encrypted data. After
132 successful implementation of side channel attack, research work implementing some appropriate countermeasures
133 against side channel attack on AES implementation and finally evaluating their performance and soundness to
134 prevent possible vulnerabilities and develop more secure systems. The work implemented AES 128-bit, 10 rounds
135 algorithm by taking input as text, image and audio.

136 12 Encryption Process when input as an Text file Decryption 137 Process when input as an Text file

138 The work implemented 128 bit AES algorithm (10 round) decryption using text as an input by measuring
139 performance parameter as time complexity which is shown in Fig. 4. Time required for decryption process is
140 2.128282 milliseconds.

141 13 Encryption Process when input as an audio file

142 The work implemented 128 bit AES algorithm (10 round) encryption using audio as an input by measuring
143 performance parameter as time complexity which is shown in Fig. 5. Time required for encryption process is
144 13.899532 milliseconds. The work implemented 128 bit AES algorithm (10 round) encryption using text as an
145 input by measuring performance parameter as time complexity which is shown in Fig. ?? Time required for
146 encryption process is 1.166557 milliseconds.

147 14 Decryption Process when input as an audio file

148 The work implemented 128 bit AES algorithm (10 round) decryption using audio as an input by measuring
149 performance parameter as time complexity which is shown in Fig. 6. Time required for decryption process is
150 20.183485 milliseconds.

151 **15 Encryption Process when input as an Image file**

152 The work implemented 128 bit AES algorithm (10 round) encryption using image as an input by measuring
153 performance parameter as time complexity which is shown in Fig. 7. Time required for encryption process is
154 61.958627 milliseconds.

155 **16 Decryption Process when input as an Image file**

156 **17 Experimental Results**

157 In this section The work presented result graph of our proposed system, implementation of the AES algorithm
158 by taking text, image and audio as input

159 **18 Conclusion Andfuture Scope**

160 Due to the increasing needs for secure communications a safer and more secured cryptographic algorithm has
161 to be proposed and implemented. The Advanced Encryption Standard (AES-128bit) is widely used nowadays
162 in many applications. In this paper, the work implemented an efficient AES128 bit encryption and decryption
163 algorithm. The execution time for AES encryption and decryption is calculated by performing 10 round functions.
164 The system presented an attack on AES software implementations. Future work will focus on investigating
165 and implementing a number of countermeasures against side channel attack on AES implementation and have
166 evaluated their performance and soundness to prevent possible vulnerabilities and develop more secure systems.

167 **19 Global**



Figure 1: 2 .

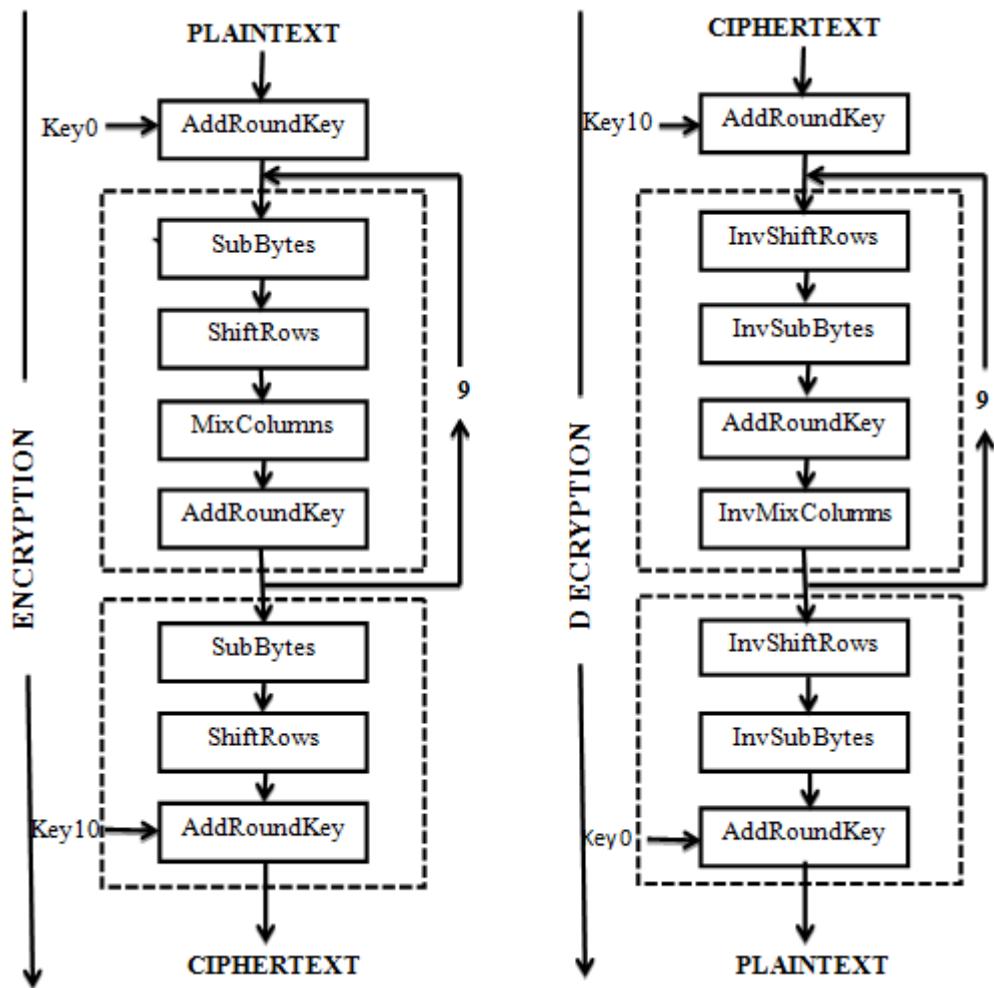
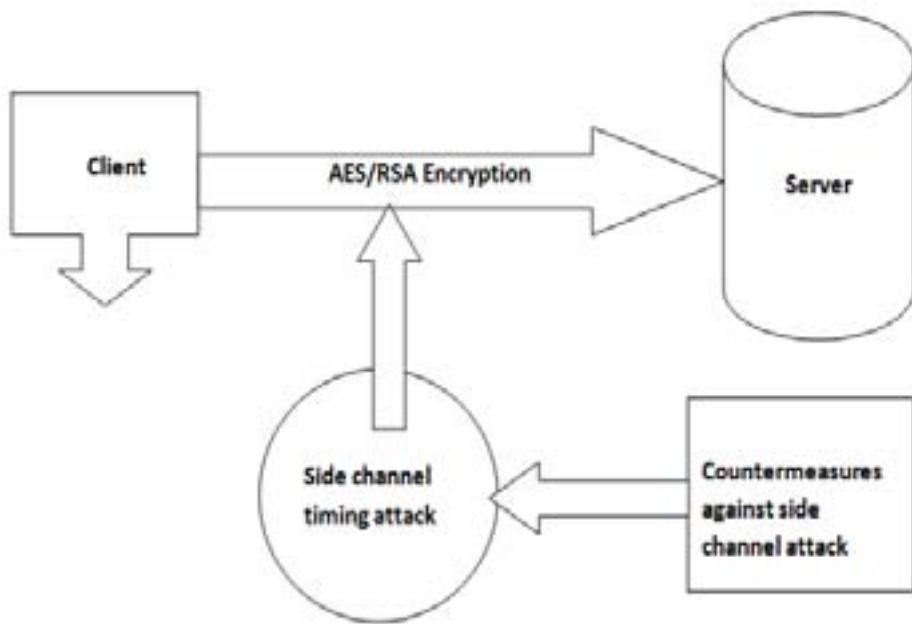


Figure 2: ©



1

Figure 3: Figure 1 :

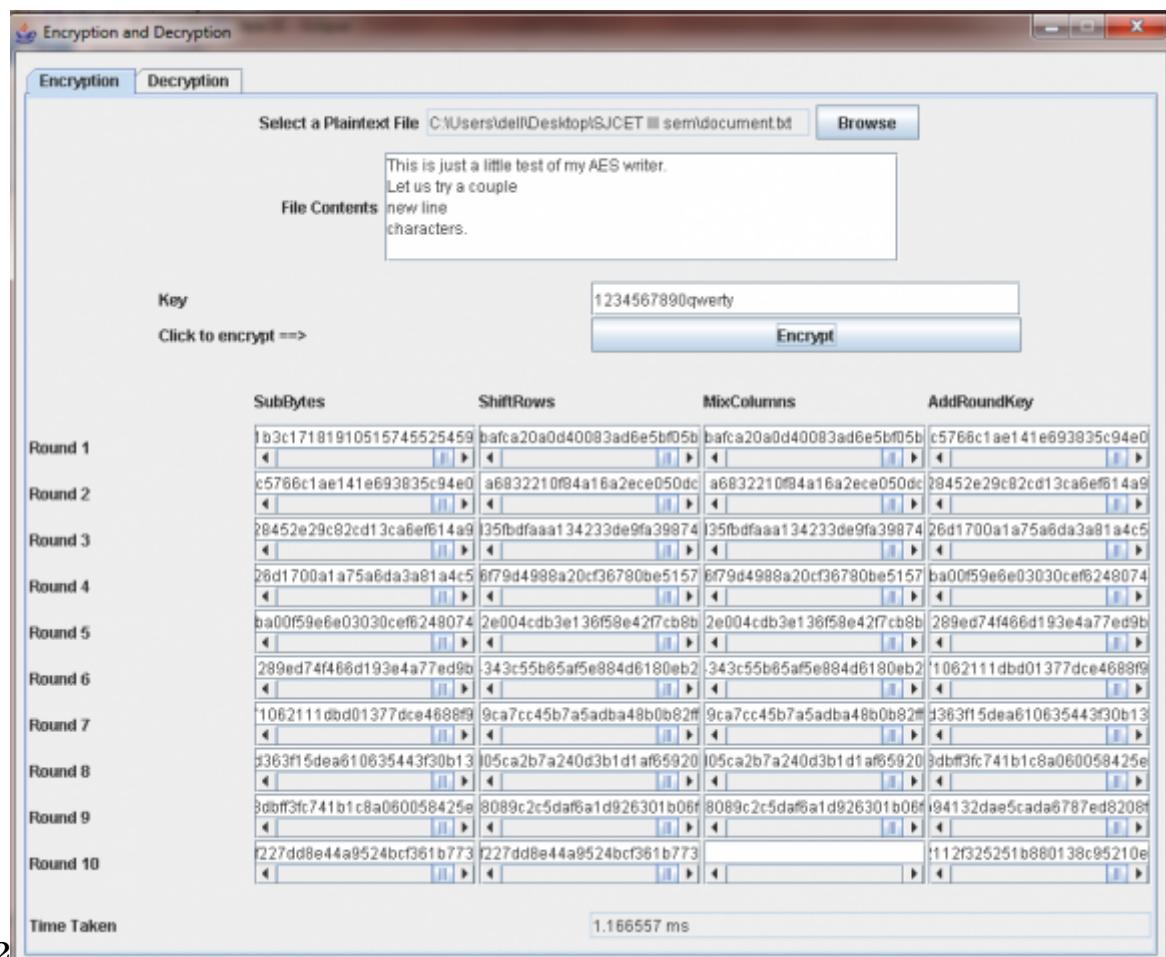
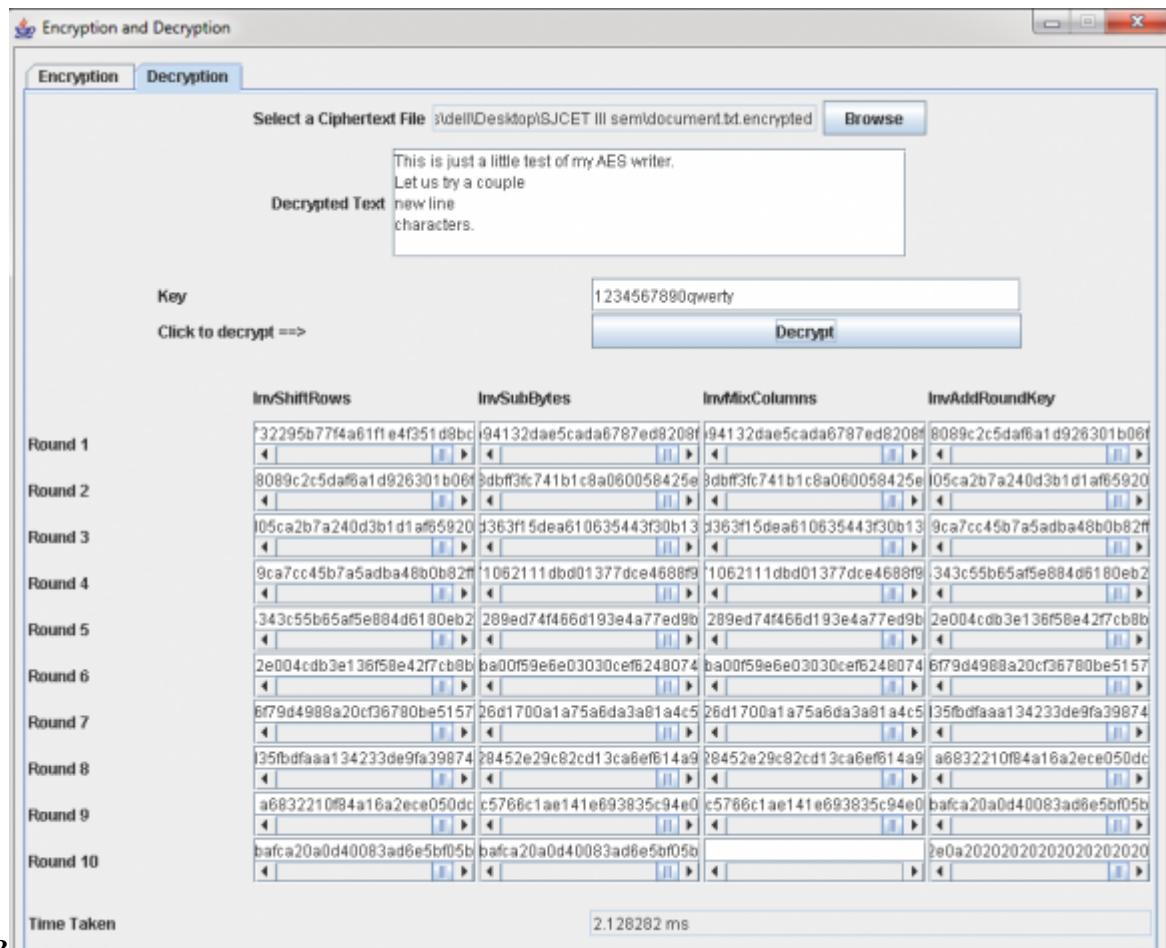


Figure 4: Figure 2 :



143

Figure 5: 14 GlobalFigure 3 :

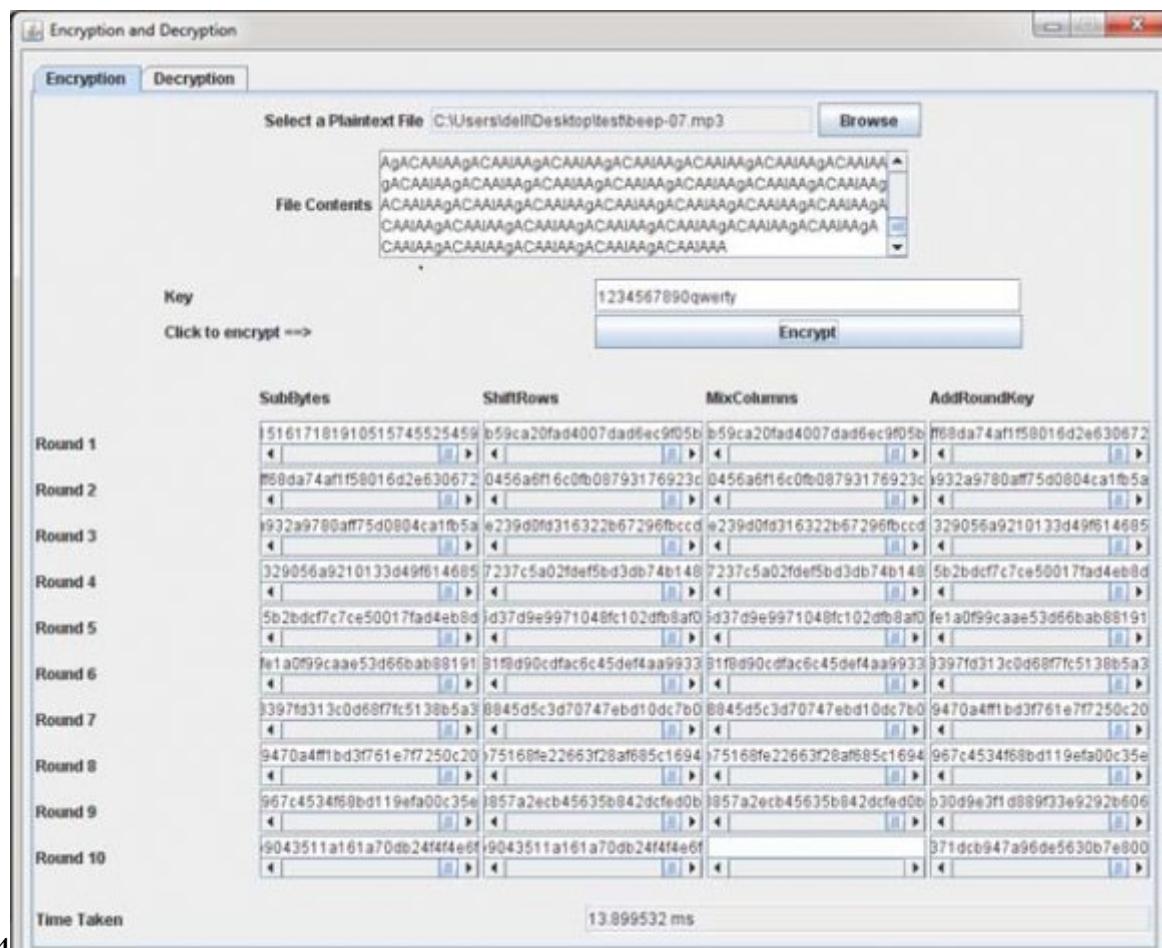


Figure 6: Figure 4 :

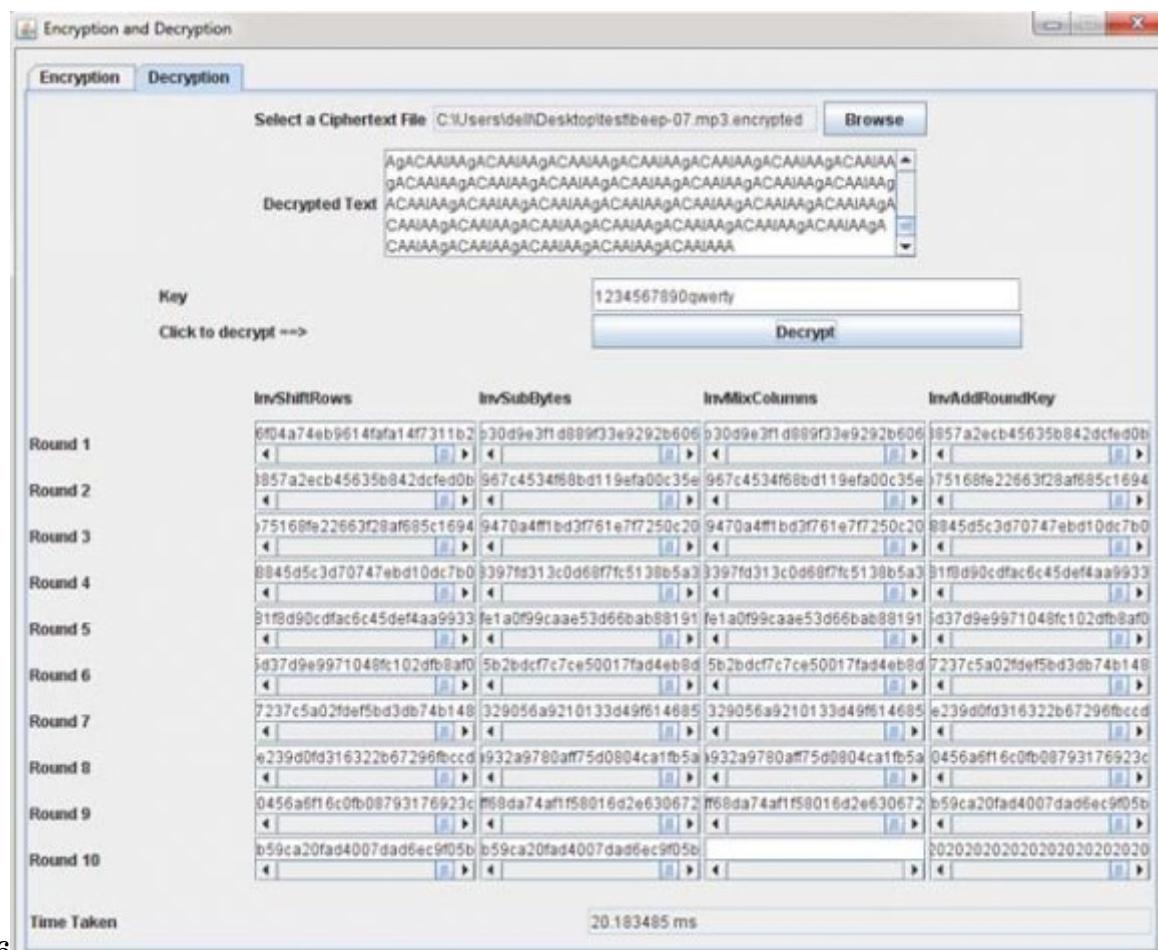


Figure 7: Figure 6 :

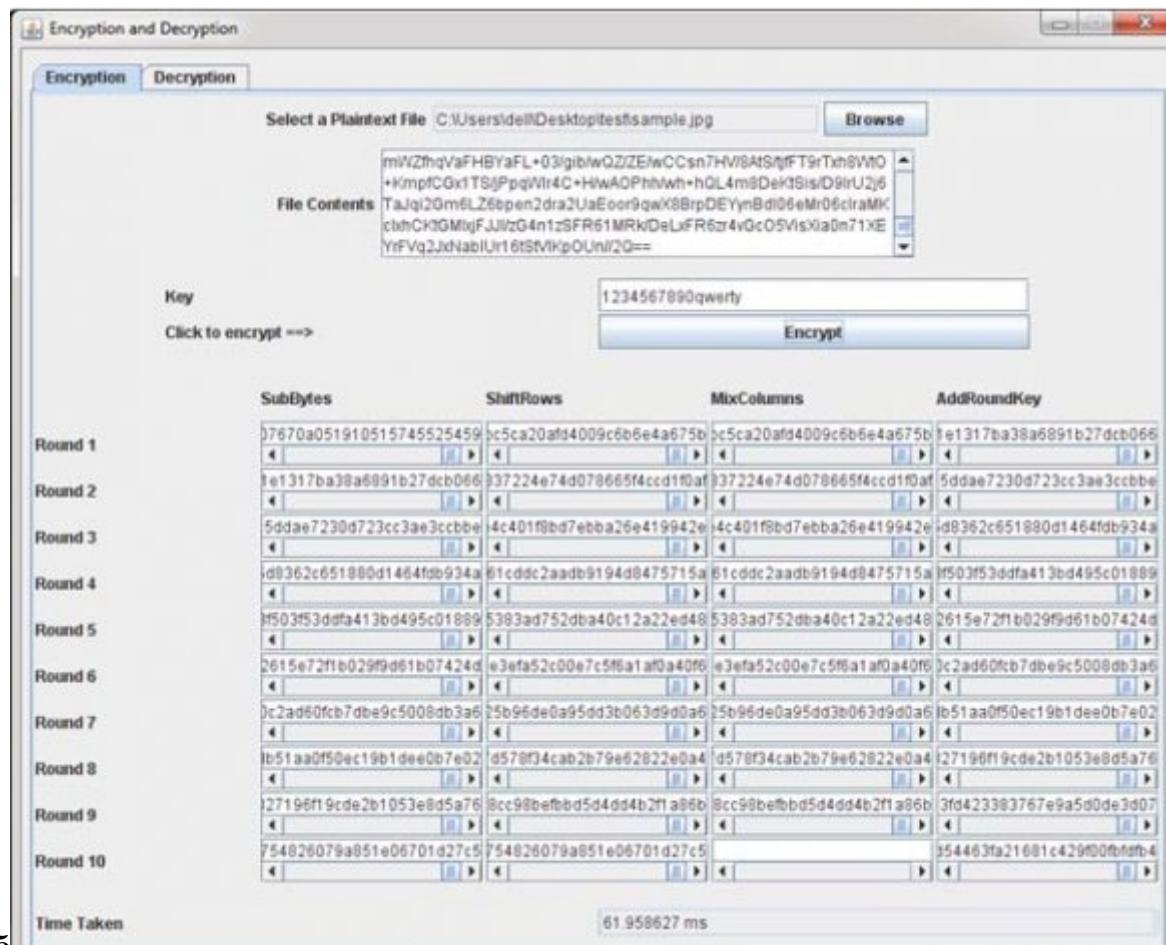
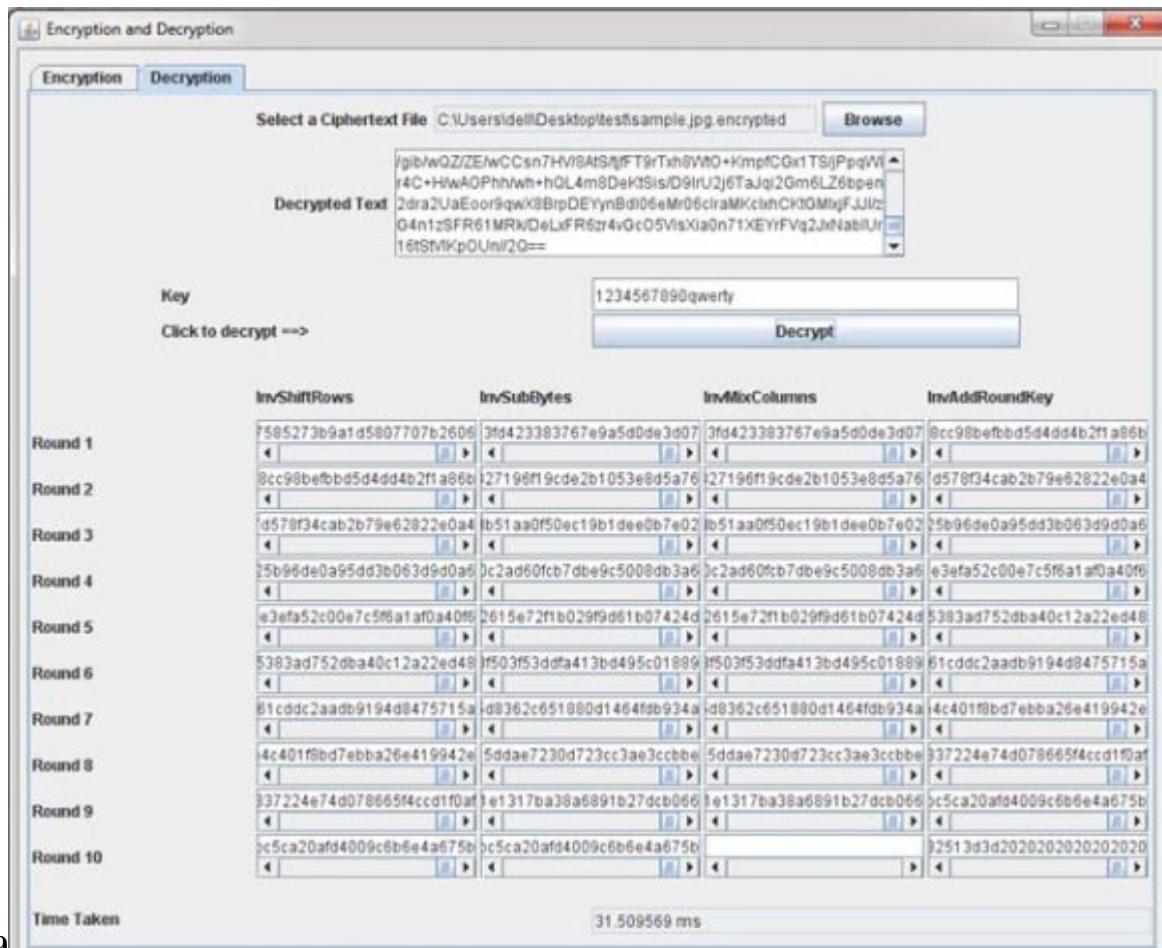
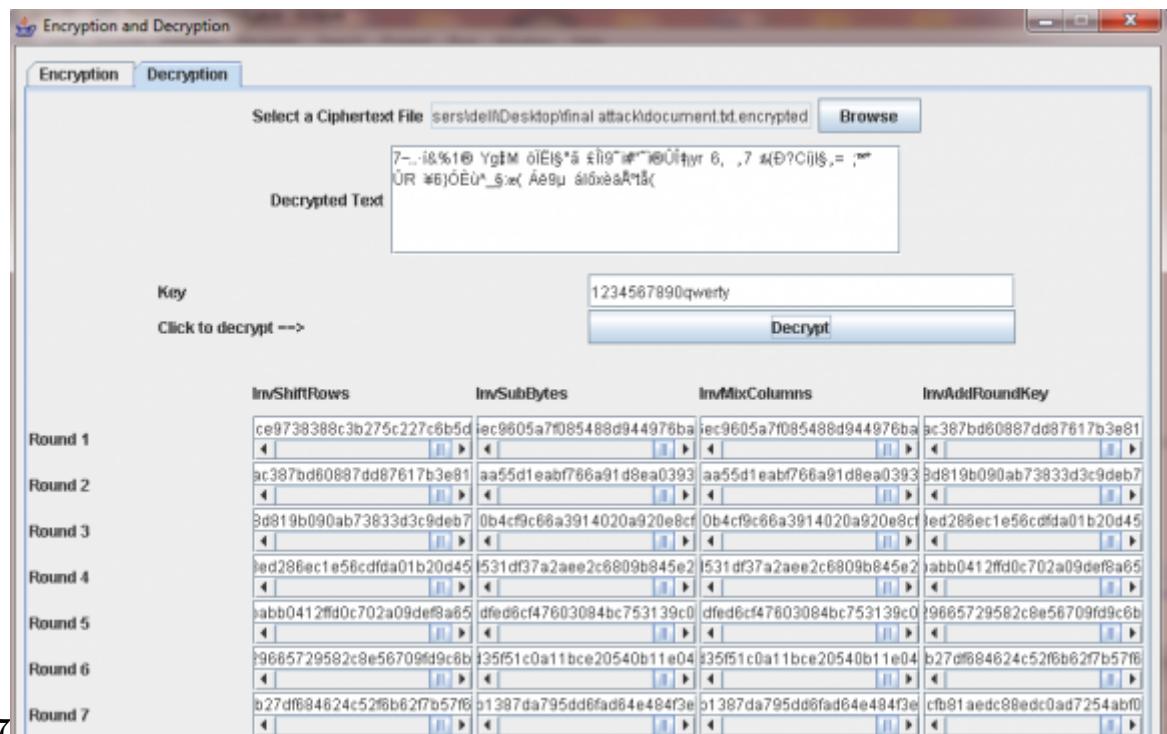


Figure 8: Figure 5 :



89

Figure 9: Figure 8 :Fig. 9 .



7

Figure 10: Figure 7 :

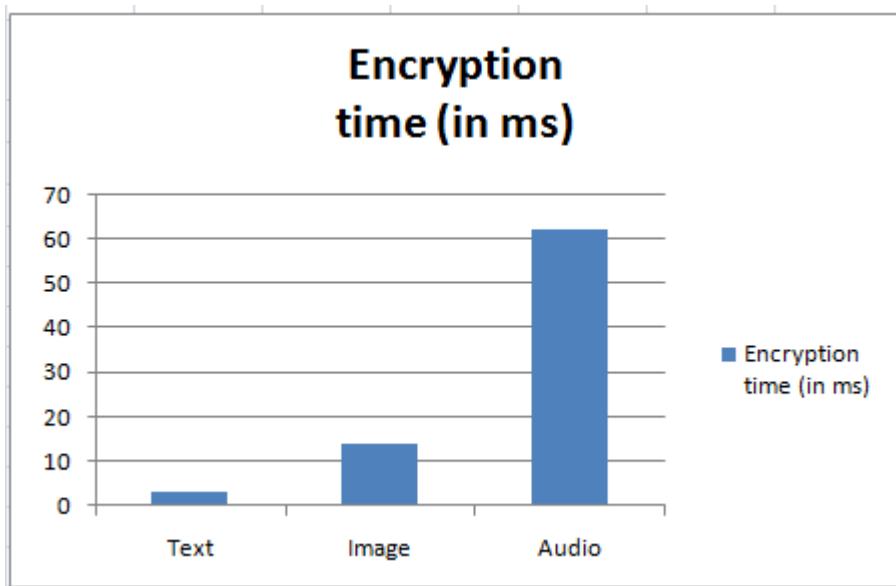


Figure 11:

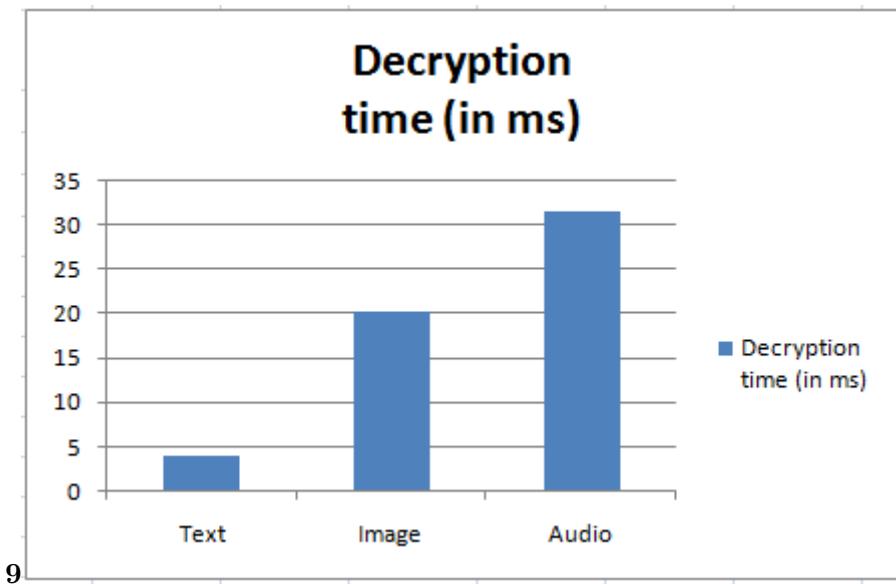


Figure 12: Figure 9 :

1

AES PARAMETERS	AES-128	AES-192	AES-256
Key Size (Bits)	128	192	256
Number of rounds	10	12	14
Plaintext box size (Bits)	128	128	128

Figure 13: Table 1 :

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